# Exploring different faulting techniques for stressing White-Box cryptography binaries



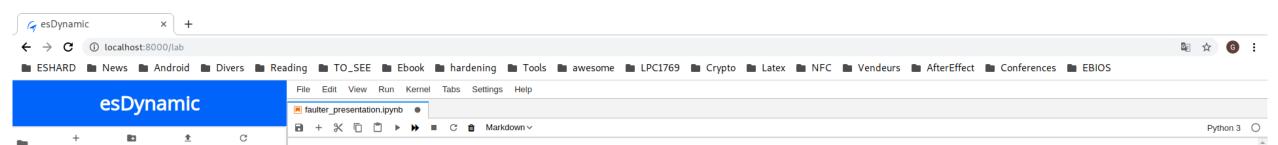
Guillaume VINET

19th May 2019

WhibOx 2019

- Binary analysis: dynamic fault injection is a powerful way to stress WBC-based solutions,
- Publications in this area remain modest, mostly due to challenging practical realisation,
- Registers, memory access can be changed in runtime leading to exploitable faulty computations,
- Nowadays, a state-of-the-art WBC security analysis must include:
  - static and dynamic fault injections
  - an efficient way to induce dynamic faulty computations: being precise and able to affect large range of instructions
  - a large range of public fault injection attacks exploiting single or multiple faults

#### How can this be achieved?

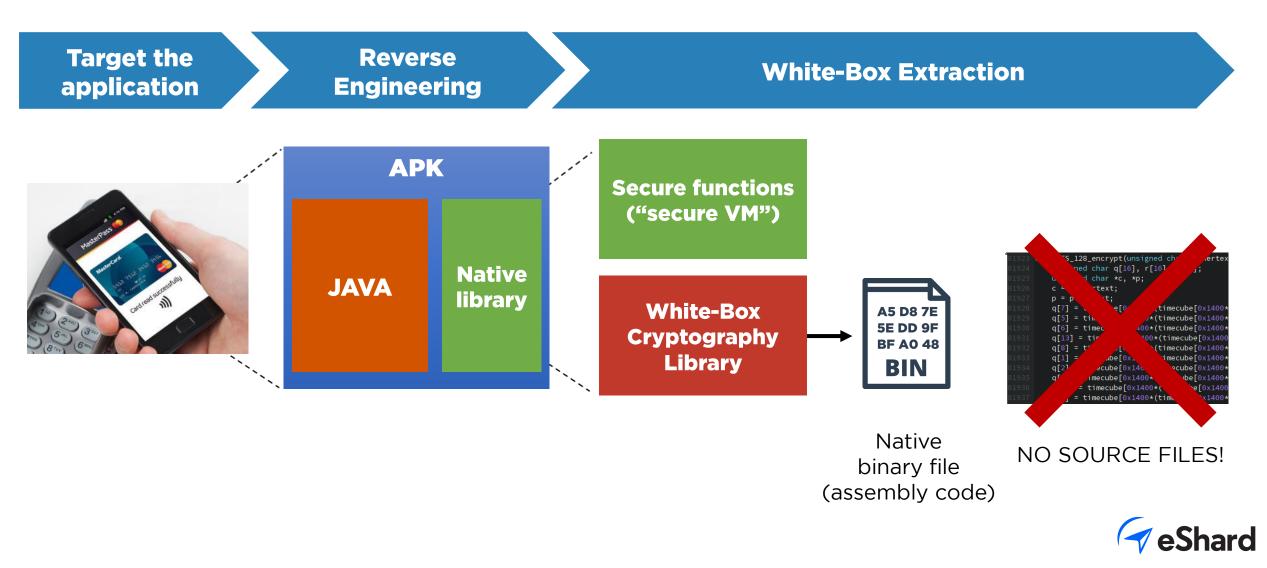


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#### Exploring different faulting techniques for stressing white-box cryptography binaries



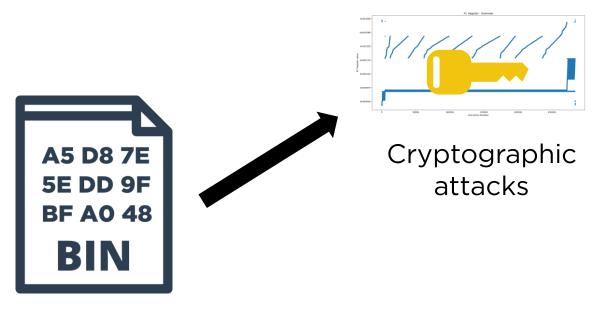
### WHERE IS THE WHITE-BOX?



### Why fault a white-box?



## WHY FAULT A WHITE-BOX?

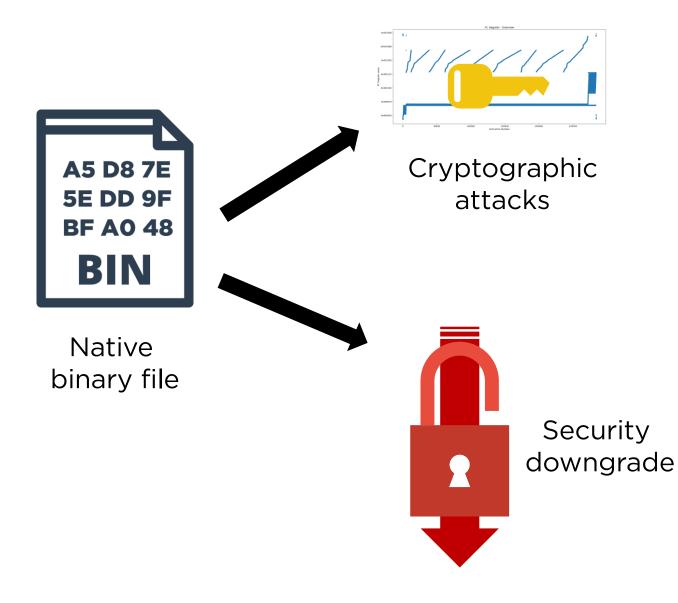


- Differential Fault Analysis (DFA)
- Safe Error
- ...

Native binary file



# WHY FAULT A WHITE-BOX?



- Differential Fault Analysis (DFA)
- Safe Error
- ...

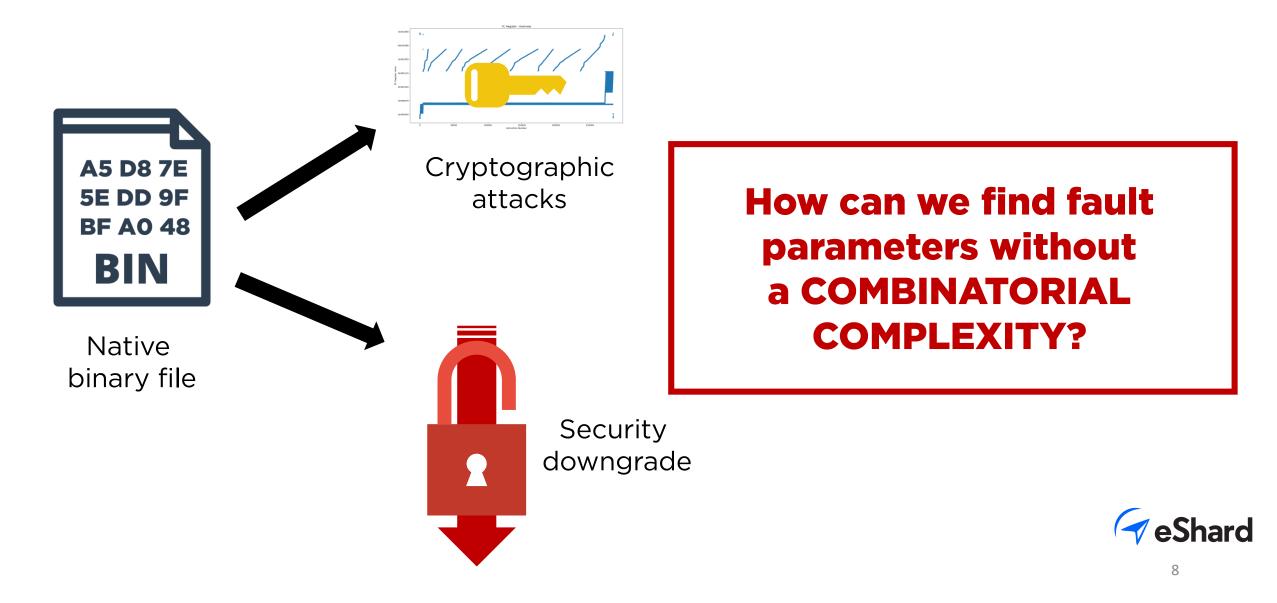
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- Defeat integrity mechanisms
- Defeat algorithm protection.
   Stuck mask value to transform a 2<sup>nd</sup> order attack to a 1<sup>rst</sup> order



### WHY FAULT A WHITE-BOX?



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	▼ How to fault a White-Box								
	Slide 0	Slide 1	Slide 2	Slide 3	Slide 4	Slide 5	Slide 6	Slide 7	Slide 8
		<ul> <li>Introduction</li> <li>How to fault a White-Box</li> <li>Requirements to fault a White-Box</li> <li>Double Fault injection on an AES White-Box</li> </ul>							
		· Co	nclus	ion				12	

#### STATIC FAULT INJECTION A5 D8 7E **B5 D8 7E OUTPUT 5E DD 9F 5E DC 9F BF AO 48 BF A2 48** BIN BIN Altered **Expected?** Genuine Altered **Faulty?** binary binary binary execution Crash?



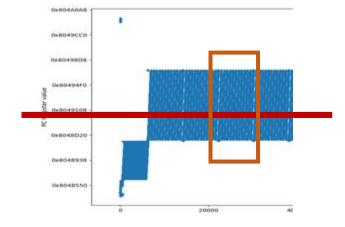
# **STATIC FAULT INJECTION**

Assets:

• Easy to implement

Drawbacks:

- Speed: how to avoid combinatorial complexity with multiple fault injections?
- Accuracy: valuable to modify table value, but not disturbing operation execution
- Anti-Fault countermeasures: fault easily detected





## **STATIC FAULT TOOLS**



Deadpool

included in



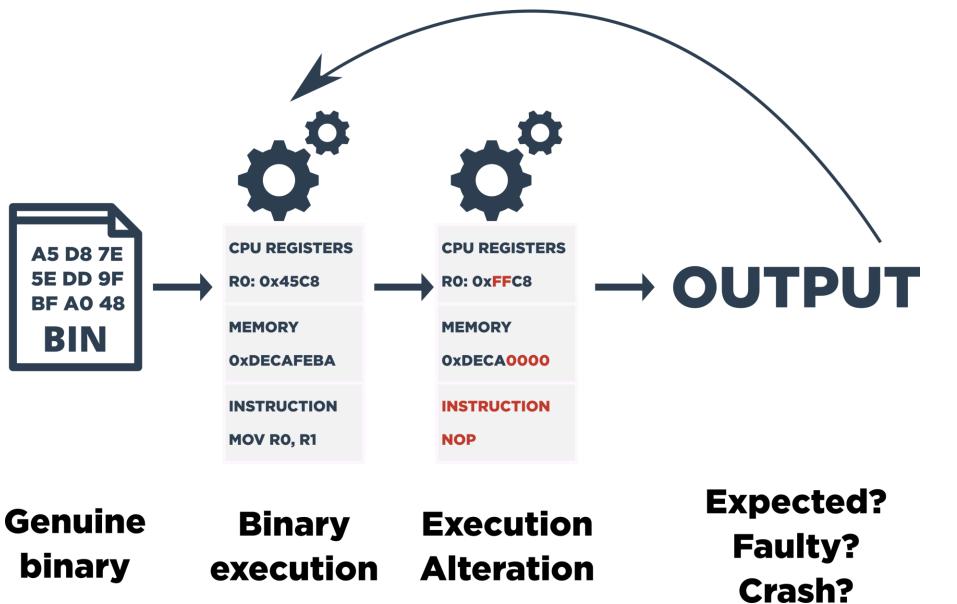
Side Channel Marvels Framework

https://github.com/SideChannelMarvels

- Python framework
- Tree strategy to inject the faults







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Assets:

- Accuracy:
  - Alter registers, memory or instructions
  - Multiple fault injection to defeat security countermeasures

Drawbacks:

- Fault Model: which fault effects must be implemented
- Speed: how to avoid combinatorial complexity with multiple fault injections?



#### Assets:

- Open source
- Use the powerful Unicorn Engine...



Unicorn



# Patch printf and putchar .plt --> return
mu.mem\_write(0x80483BC, "\xc3")
mu.mem\_write(0x80483EC, "\xc3")

def hook\_mem\_access\_fault(uc, access, address, size, value, user\_data):
 global output, evtId, fault
 evtId += 1
 pc = uc.reg\_read(UC\_X86\_REG\_EIP)

targetId = user\_data[0]
if access == UC\_MEM\_READ:
 value = u32(uc.mem\_read(address, size))
 if should\_fault(evtId, targetId, fault, address, size):
 print "FAULTING AT ", targetId
 # ALready faulted this time
 fault = False
 # Random bit in this event
 bitfault = 1 << random.randint(0, size\*8 -1)
 uc.mem\_write(address, pack(value ^ bitfault, size))</pre>

# At this PC the pushes a byte of output to the stack
if pc == 0x08049e4f:
 output.append(value)

Know where to recover the ciphertext once the fault was injected



Source https://www.riscure.com/uploads/2017/09/eu-15-sanfelix-mune-dehaas-unboxing-the-white-box-wp\_v1.1.pdf<sup>16</sup>

Call to external libraries must be implemented/patched

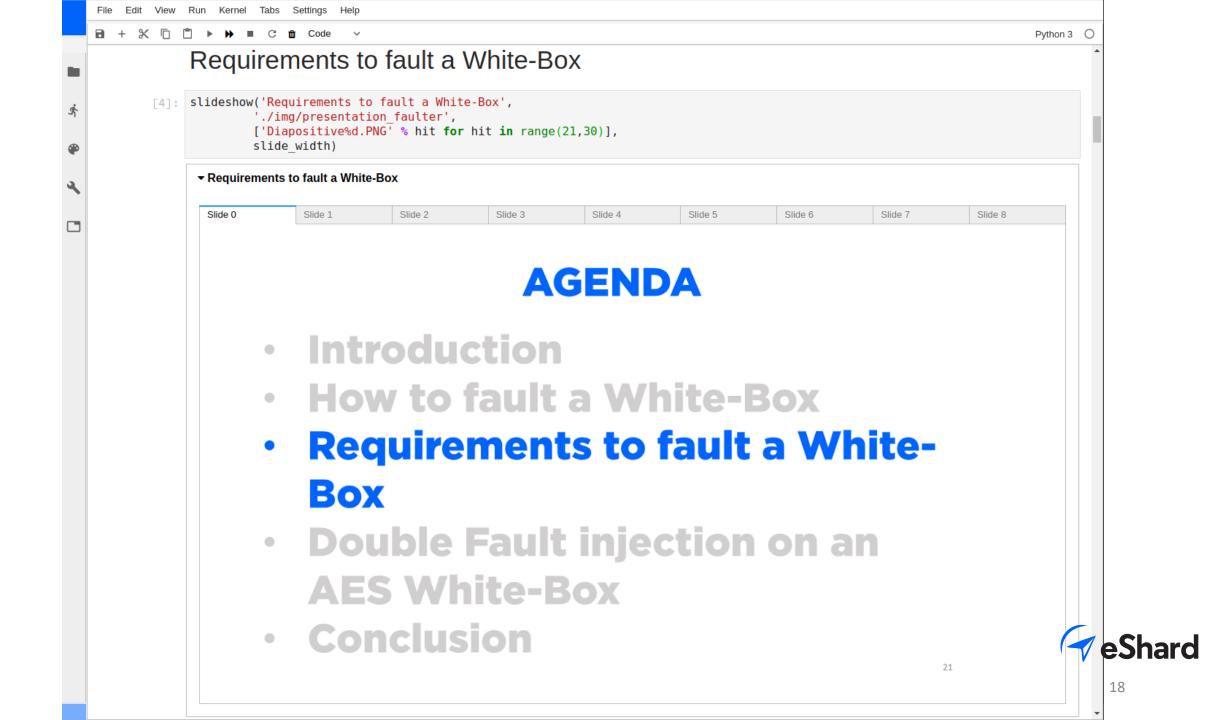
#### Assets:

- Open source
- Use the powerful Unicorn Engine...

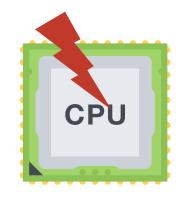


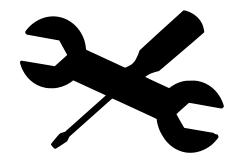
- Unicorn
- Drawbacks:
- ... that needs reverse engineering
- Executable/Library must be instrumented by a script
- The Unicorn emulation is slow





#### Dynamic fault injection

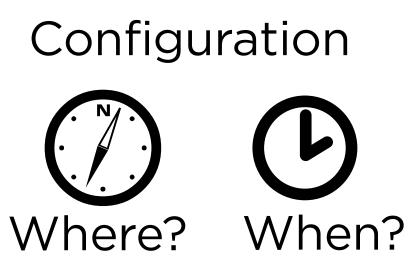






Speed

Relevant Fault Models





#### Faults models:

Register modification



# WHAT ARE WE LOOKING FOR?

Faults models:

- Register modification
  - Data Flow Modification



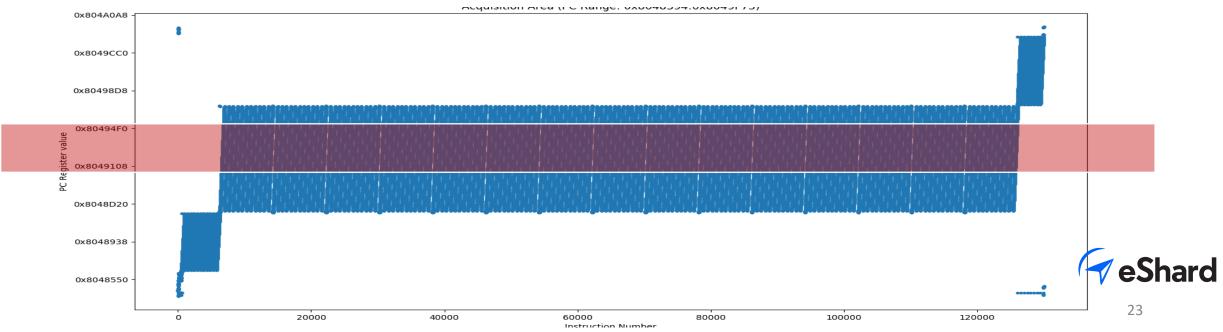
Faults models:

- Register modification
  - Data Flow Modification
  - Control Flow Modification with Program Counter Register



#### Where to fault:

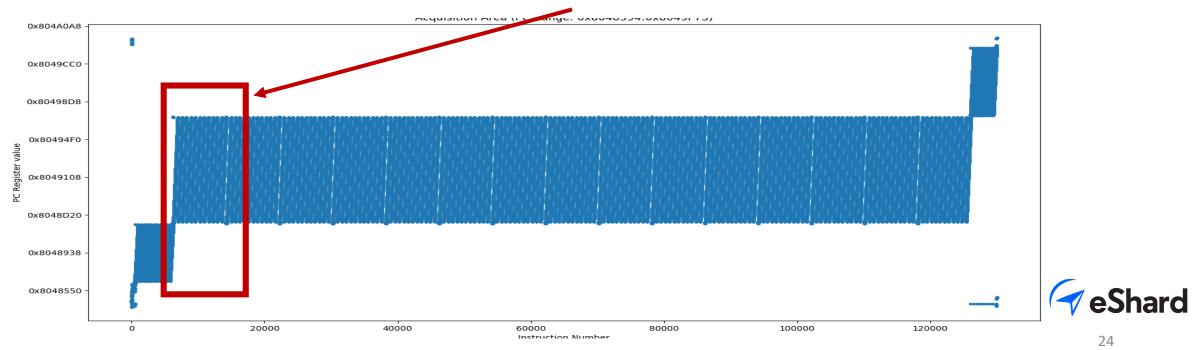
- Filtering based on:
  - Program Counter
  - Kind of instruction: mov, add ...



When to fault: pattern detector

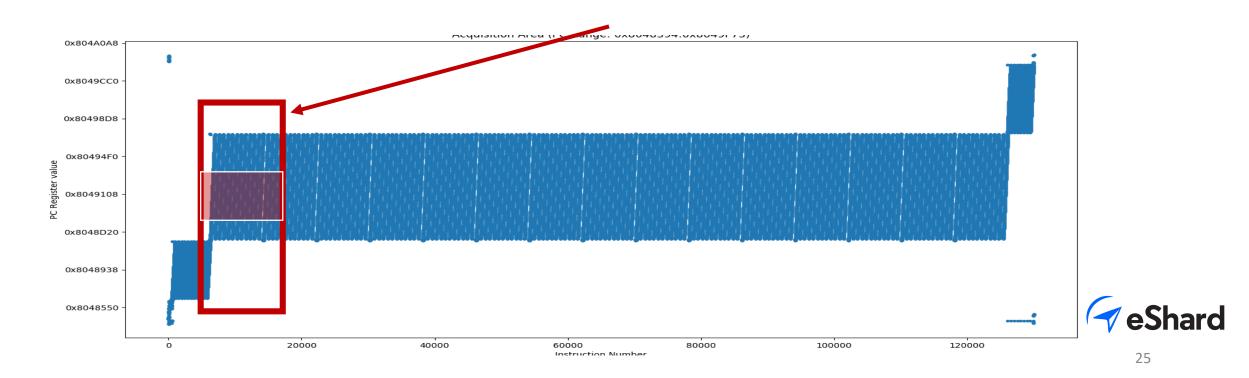
• We must start the faults when the correct PCs are reached ...

... and we also stop them in the same way



When to fault: pattern detector

 It can be combined with a filtering based on Program Counter value or Instruction kind



# eShard esFaulter

- Dynamic register modification
  - Data flow disturbance
  - Control flow disturbance
  - Multi-fault injection
  - Fault&trace capabilities
  - Filtering and trig&act capabilities



- X86, x86\_64, ARM support
- Takes advantage of Qemu speed



included in

esDynamic Platform

	Double Fault	injection on	an AES White-Box	
[5]:		ntation_faulter',	it <b>in</b> range(30,34)],	
	- Introduction			
	Slide 0 Slide 1	Slide 2	Slide 3	
			AGENDA	
	• In	troduc	tion	
	• H	ow to f	ault a White-Box	
	• R	equirer	nents to fault a Wh	nite-
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	• D	ouble F	-ault injection on a	n
	<b>A</b>	ES Wh	ite-Box	
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# THE CHALLENGE

- Attack an AES White-Box implementation
- Configuration:
  - CPU i7-7560U, 2.4GHz dual core
  - 16 GB of RAM (we not need so much)
  - SSD NVMe
- Double fault injection
- Key recovery from the faulty outputs with a DFA of Piret (with 4 modified bytes in a specific way)



# THE CHALLENGE

- Attack an AES White-Box implementation
- Double fault injection
- Key recovery from the faulty outputs with a DFA of Piret (with 4 modified bytes in a specific way)

#### How can we find faults parameters without a COMBINATORIAL COMPLEXITY ?

nb\_ins x nb\_fault\_model x nb\_target x nb\_input x nb\_area



#### ILLUSTRATION

#### AES-128 X86-64 architecture

\$./cipheraes 06 1F C9 F5 88 B2 F9 D2 00 19 86 82 2C 12 11 79 message: 061fc9f588b2f9d2001986822c121179 cipher: 14ed01ea7ce2a551c9791ae85c7cecf4

Differential Fault Analysis with a double fault injection attack:

- Data flow disturbance
- Control flow disturbance



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Our target is a x86-64 architecture executable.

#### []: !file ./binary/cipheraes

./binary/cipheraes: ELF 64-bit LSB executable, x86-64, version 1 (SYSV), dynamically linked, interpreter /lib64/ld-linux-x86-6
4.so.2, for GNU/Linux 2.6.32, BuildID[sha1]=b4bdf121200bbae155c26bc03d474e9f29eb980f, stripped, with debug\_info

Let's see how to use it.

[7]: !./binary/cipheraes

cipheraes B01 B02 B03 B04 B05 B06 B07 B08 B09 B10 B11 B12 B13 B14 B15 B16, BXX is hexadecimal arguments problem

We need to provide a 16-byte hexadecimal input, and then get the ciphered message.

#### [8]: !./binary/cipheraes 061FC9F588B2F9D2001986822C121179

cipheraes B01 B02 B03 B04 B05 B06 B07 B08 B09 B10 B11 B12 B13 B14 B15 B16, BXX is hexadecimal arguments problem

The input format must be carefully respected in order to get the ciphered message.

#### [9]: !./binary/cipheraes 06 1F C9 F5 88 B2 F9 D2 00 19 86 82 2C 12 11 79

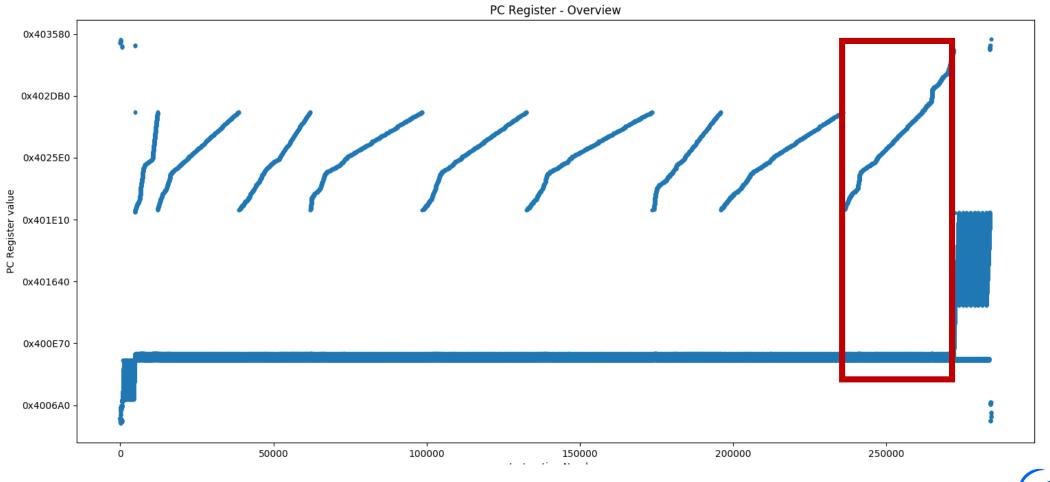
message: 061fc9f588b2f9d2001986822c121179
cipher: 14ed01ea7ce2a551c9791ae85c7cecf4

We understood how the binary works, let see now how to use esFaulter.



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	WHELE ID ALLACK?	
[10]:	slideshow('Where to attack?', './img/presentation_faulter', ['Diapositive%d.PNG' % hit <b>for</b> hit <b>in</b> range(34,36)], slide_width)	
	✓ Where to attack?	
	Slide 0 Slide 1	
	FIRST STEP	
	Where inject faults?	
	<ul> <li>only the binary itself, not external system libraries</li> </ul>	
	How to know where to inject faults?	
	<ul> <li>Trace memory access or registers</li> </ul>	
	<ul> <li>Display them to see distinguishable patterns</li> </ul>	
	<ul> <li>Program Counter (PC), address of executed instruction, tracing is a good start</li> </ul>	
	instruction, tracing is a good start	
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#### **SIMPLE FAULT INJECTION**



**AES round patterns? Let's attack the last one** 



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		Before performing fault injections, we need to know where to inject them.					
		We are going to use esTracer to acquire the PC register evolution, and then, we analyze its graphical representation.					
>		First, we configure the binary handler configuration object. It eases the management of the binary to attack.					
•	[11]:	<pre>from essva.binary_handler import BinaryHandlerConfiguration import re</pre>					
3		<pre>bin_hand = BinaryHandlerConfiguration()</pre>					
		<pre>bin_hand.algo = "AES-128" bin_hand.cmd = "./binary/cipheraes"</pre>					
		We need to indicate how to give a correct input to the program, and as well how to interpret its response.					
	[12]:	<pre>def process_input(block):     return " ".join("%02X" % hit for hit in bytes.fromhex(block)).lower()</pre>					
		<pre>def process_output(output):     return re.match("message: [a-f,0-9]{32}\ncipher: ([a-f,0-9]{32})?", output).group(1)</pre>					
		<pre>bin_hand.process_input = process_input bin_hand.process_output = process_output</pre>					

We are ready to use esTracer!!!

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               We trace the program scanning only the PC register.
        [13]: from essva.tracer import Tracer
               tracer = Tracer(bin hand)
               By default, it will trace everything, so we configure it:

    we want to trace only the PC register.

                 • we focus the tracing on the binary itself, the PC range value can be retrieved for instance with readelf.
              tracer.registers
                                        = ["pc"]
        [14]:
               tracer.filt pre.in pc.ranges = [[0x400458, 0x4034DC]]
               We indicate where the traces will be generated.
               tracer.directory out = "traces"
        [15]:
               We generate one trace. esTracer can:

    either automatically generate a random input,

    either use a list of user input. We will use this option.

               plain input = "061FC9F588B2F9D2001986822C121179"
        [16]:
               tracer.generate_traces([plain_input])
        [17]:
               Generating 1 trace(s). Please wait...
```

Elapsed: 00:00:00 Remaining: 00:00:00

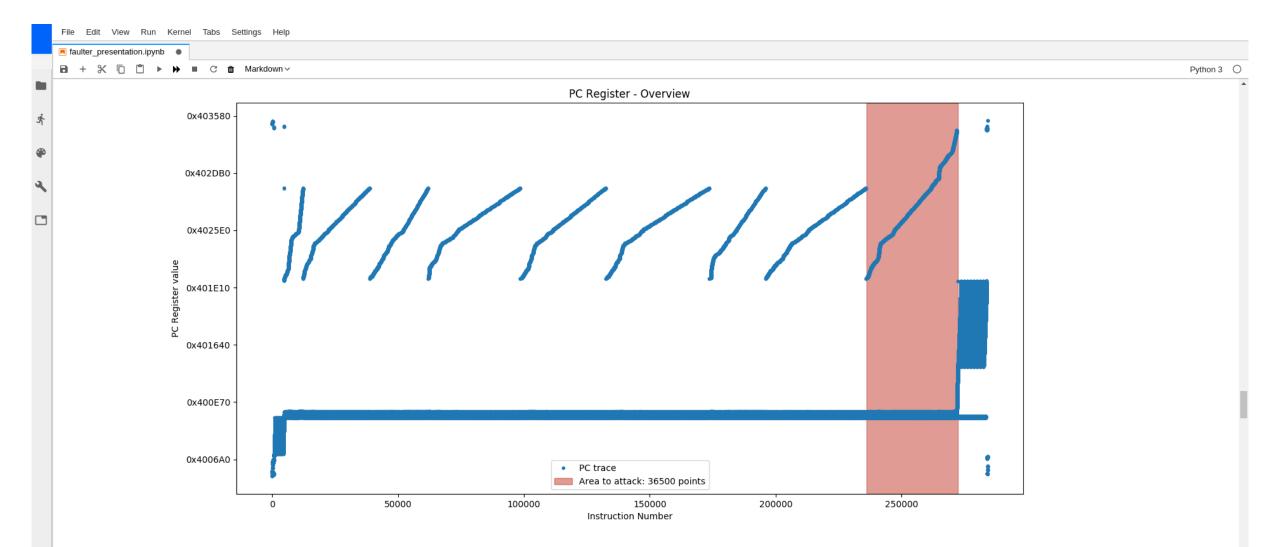
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You can explore the PC trace by zooming in.

We see 9 repeated patterns, it might correspond to the AES round (the two last rounds are likely to be merged together). We decide to attack the last pattern area.



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#### Fault injection campaign

We will inject a simple fault attack to perform a DFA of Piret, it means we have to obtain outputs with 4 faulty bytes.

We import the esFaulter object, and link it to the binary handler configuration.

[21]: from essva.faulter import Faulter import essva.sva constants as const

faulter = Faulter(bin\_hand)

We indicate where to generate the log.

[22]: faulter.directory\_out = "faulter\_outputs\_one\_fault"

We use two trig&act to focus on the last pattern area:

- · when the PC 0x400cd2 was called 5297 times, we start to inject faults
- when the PC 0x400c5c was calld 4938 times, we stop the fault injection

See the presentation "Optimize your binary tracing: an example with an ECDSA implementation" for more details about the trig&act.

[23]: nb\_inst\_start, nb\_inst\_end = 236000, 272500
pc\_start, pc\_end = int(pc\_np[nb\_inst\_start]), int(pc\_np[nb\_inst\_end])
pc\_start\_count, pc\_end\_count = len(np.where(pc\_np[:nb\_inst\_start+1] == pc\_start)[0]), len(np.where(pc\_np[:nb\_inst\_end+1] == pc\_end

[24]: trig\_and\_act\_start\_fault\_injection = [pc\_start, pc\_start\_count, const.TAA\_F\_START\_FAULT]
trig\_and\_act\_stop\_fault\_injection = [pc\_end, pc\_end\_count, const.TAA\_F\_STOP\_FAULT]

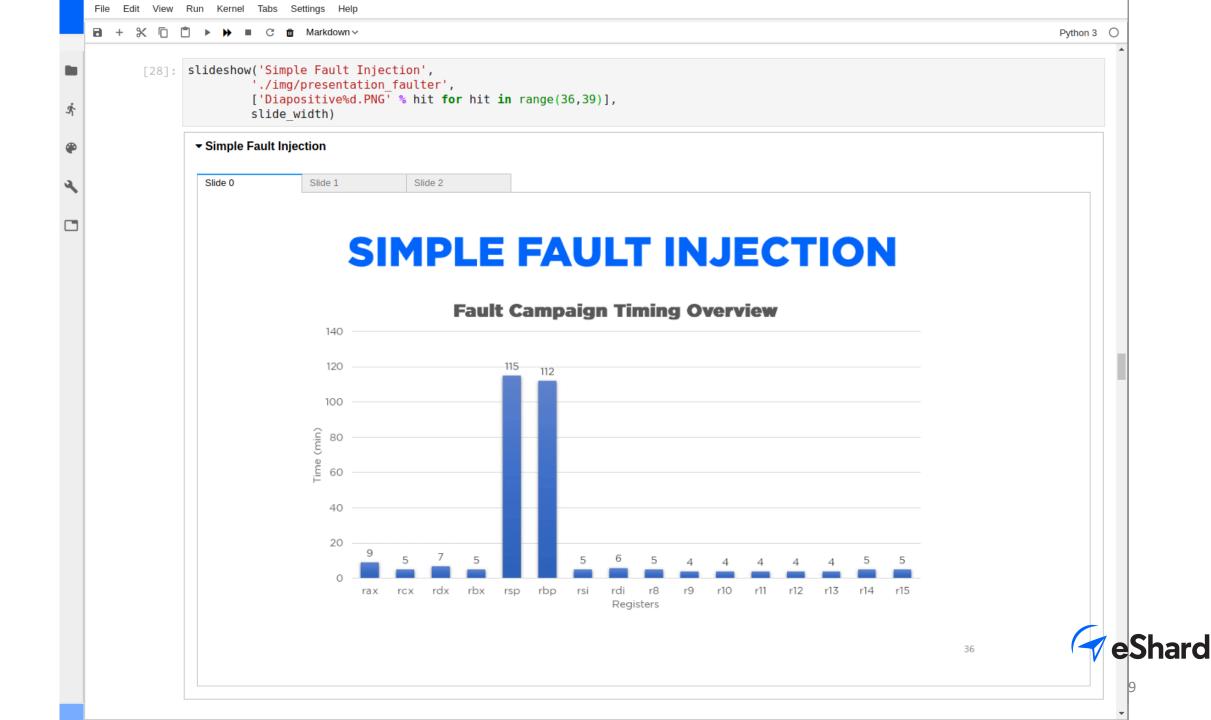


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                We are going to fault all the registers, excepting the PC.
               reg_lst = ["rax", "rcx", "rdx", "rbx", "rsp", "rbp",
À.
                             "rsi", "rdi", "r8", "r9", "r10", "r11", "r12",
                             "r13", "r14", "r15"]
                To configure a fault, we use the FaultCfgMono object:
                  · we indicate the register to fault
                  · we indicate which fault model to use, in our case we set the register to zero

    we indicate when the fault must be injected with the two trig&act created previously

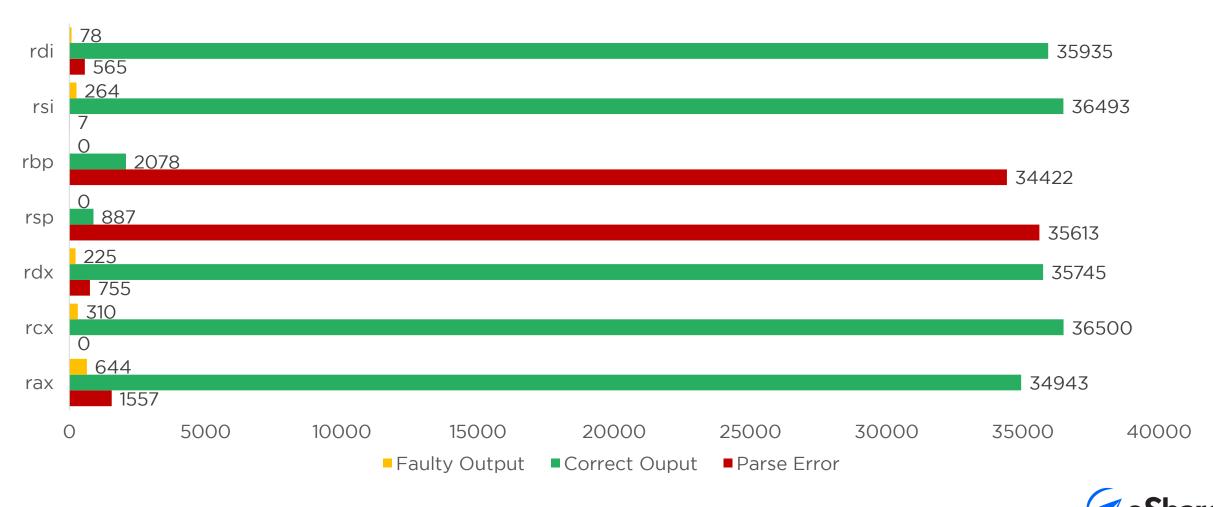
                Then, it is linked to esFaulter with faulter.faults opt that is a list of FaultCfgMono object:
                  · in that way, you can easily perform simple or multiple fault injection
                  · esFaulter analyzes the configuration of each fault to make automatic and exhaustive fault injection
                from essva.faulter import FaultCfgMono
                for register in reg lst:
                     fault 1 = FaultCfgMono(bin hand)
                     fault 1.trig and acts = [trig and act start fault injection,
                                                 trig and act stop fault injection]
                     fault 1.register = register
                     fault 1.model = const.FM SET ZER0
                     faulter.faults opt = [fault 1]
                     faulter.directory_out = "fault_1_%s" % register
                     faulter.generate faults(plain input)
```





## **SIMPLE FAULT INJECTION**

**Output Classification** 



No effect for the other registers (rbx, r8, r9, r10, r11, r12, r13, r14, r15)

# SIMPLE FAULT INJECTION



14 campaigns faulting one register (rsp/rbp not included)

511,000 injected faults

~76 min (multi-thread not used)

~112 injected faults by second



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ŝ,		RSP Register Fault analysis We analyze the log for the rsp fault injection. Instead of looking at the log file, we can use a log parser to have a pretty view.	•
@ ~	[]:	<pre>from essva.esdynamic.faulter_output import FaulterLogParser import glob import re</pre>	
		<pre>logfile = glob.glob("./fault_1_rsp/*.bin")[0] parser = FaulterLogParser(logfile, output_parser = bin_hand.process_output) parser.parse_file() # Print faulty output parser.get_faulty_output()</pre>	

Most of the fault leads to the crash of the application. It is an expected behavior since we set the stack pointer to 0. The same behavior happens for RBP, which is the base pointer.

#### RAX Register Fault analysis

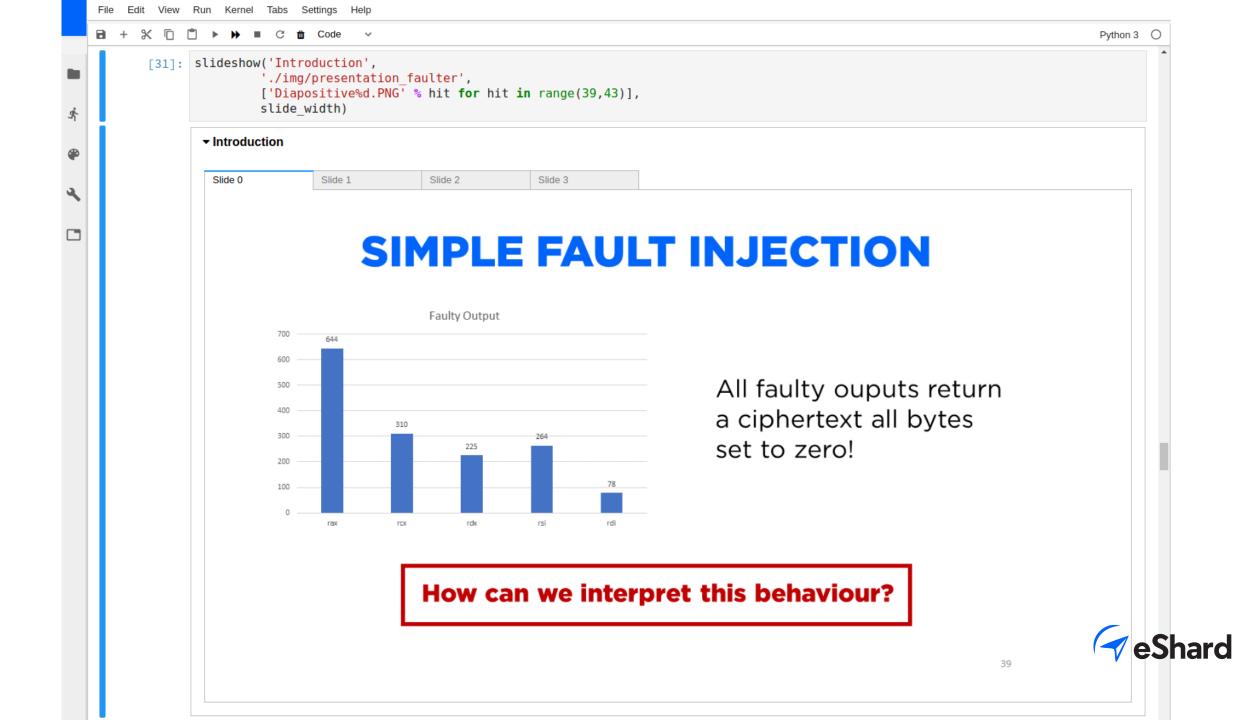
We analyze the log for the rax fault injection.

```
]: from essva.esdynamic.faulter_output import FaulterLogParser
import glob
import re
```

logfile = glob.glob("./fault\_1\_rax/\*.bin")[0]
parser = FaulterLogParser(logfile, output\_parser = bin\_hand.process\_output)
parser.parse\_file()
# Print faulty output
parser.get\_faulty\_output()



The program never crashed. But, all the faults have the same effects: we obtain a block of 16 bytes set to 0.



# HOW TO INTERPRET THE FAULTS?

Correct/ Faulty output analysis



Execute algorithm:

- To recover the key
- Or to detect in which round the fault was injected
- A lot of public algorithm available, but if they fail it gives no information



# HOW TO INTERPRET THE FAULTS?

Correct/ Faulty output analysis

Reverse engineering



Execute algorithm:

- To recover the key
- Or to detect in which round the fault was injected
- A lot of public algorithm available, but if they fail it gives no information
- Understand the effect of the fault on the program execution
- Give a way to understand very accurately the fault but it requires reverse engineering skills



# HOW TO INTERPRET THE FAULTS?

Correct/ Faulty output analysis

Reverse engineering



Execute algorithm:

- To recover the key
- Or to detect in which round the fault was injected
- A lot of public algorithm available, but if they fail it gives no information
- Understand the effect of the fault on the program execution
- Give a way to understand very accurately the fault but it requires reverse engineering skills

Fault & Trace



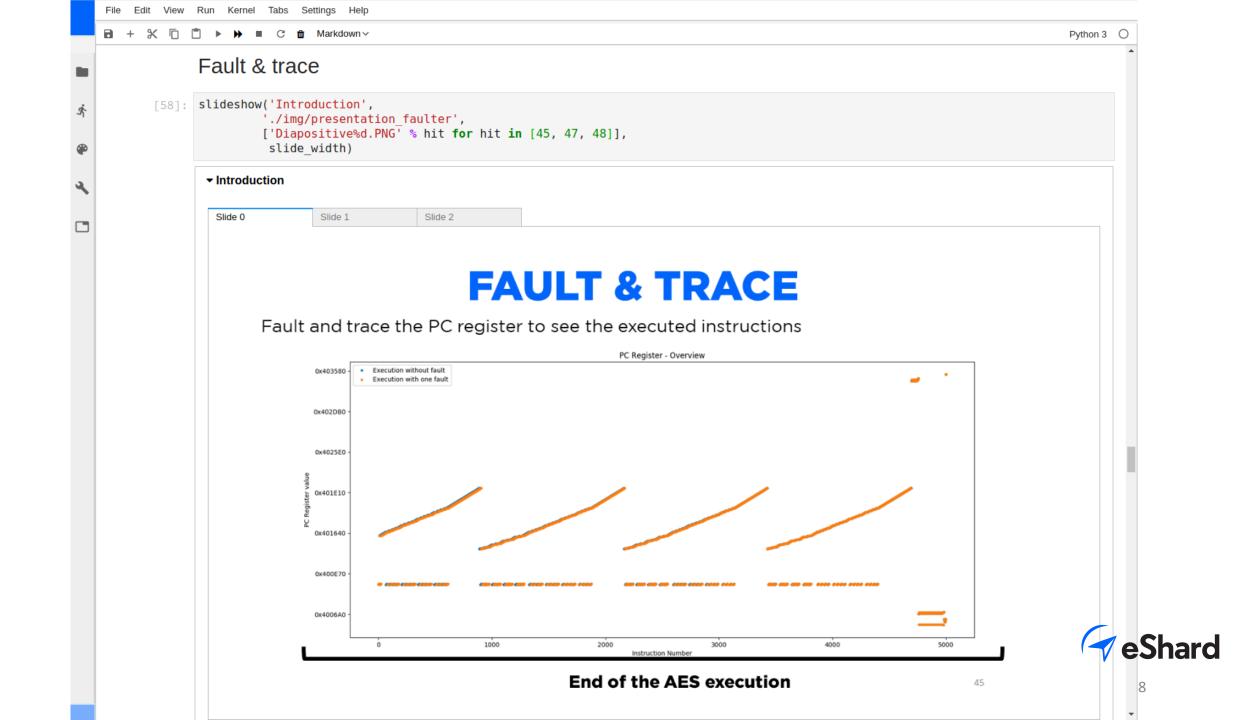
- Fault and trace at the same time (memory access, Program Counter registers ...)
- Give a visual way to understand accurately the fault effect without reverse engineering skills

## LET TRY THIS WAY



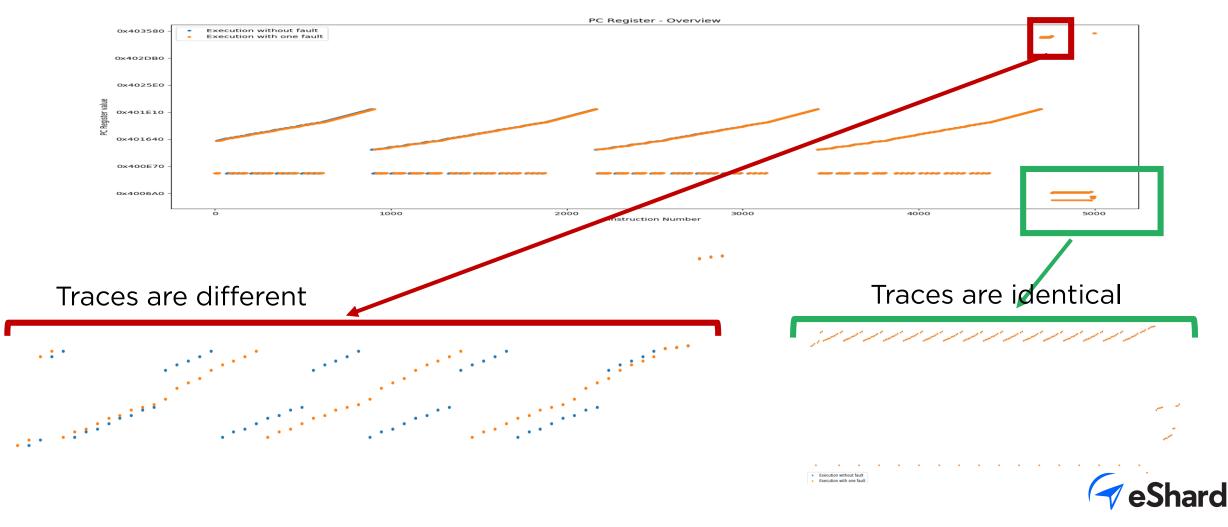
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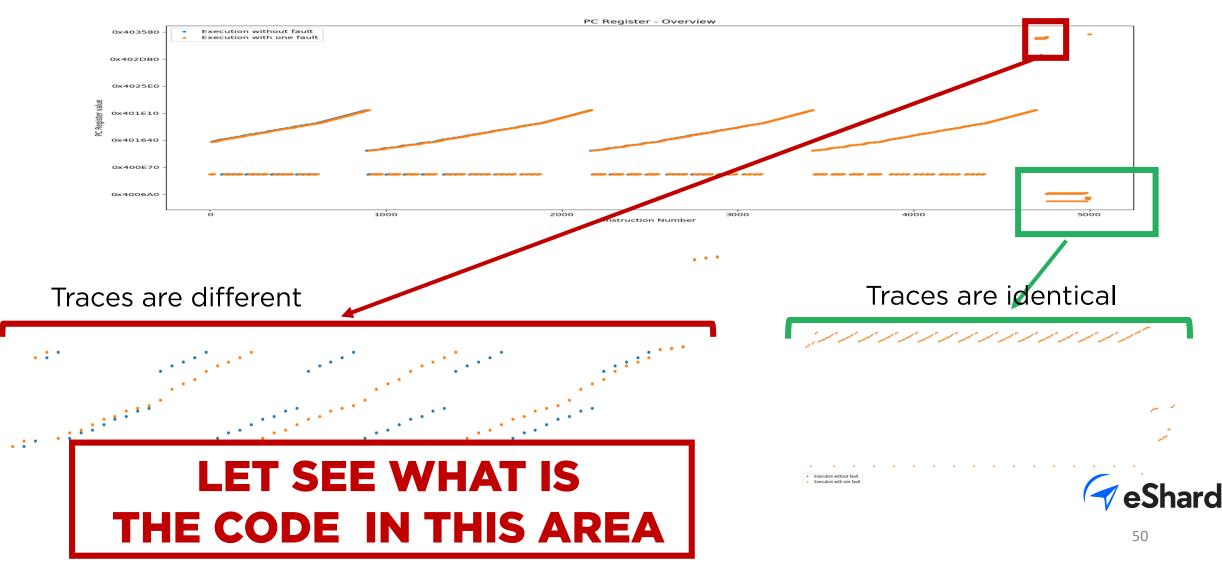
## **FAULT & TRACE**

Fault and trace the PC register to see the executed instructions



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#### Fault & trace

It is very easy to fault and trace:

- you create a tracer object
- you configure it
- · and you link it to the faulter
- automatically, for each injected fault, a trace will be generated.

We have already a tracer recording the PC, so we will use it:

[32]: faulter.tracer = tracer

We analyze the trace when we fault rax register.

- We will not once again inject 36500 faults, and create 36500 traces.
- We only need to inject one fault leading to a zero output.

So, we analyze a log to get a PC value triggering a zero output.

This PC value might be called several time in the area we attack, so we use a trig&act to fault it just once.

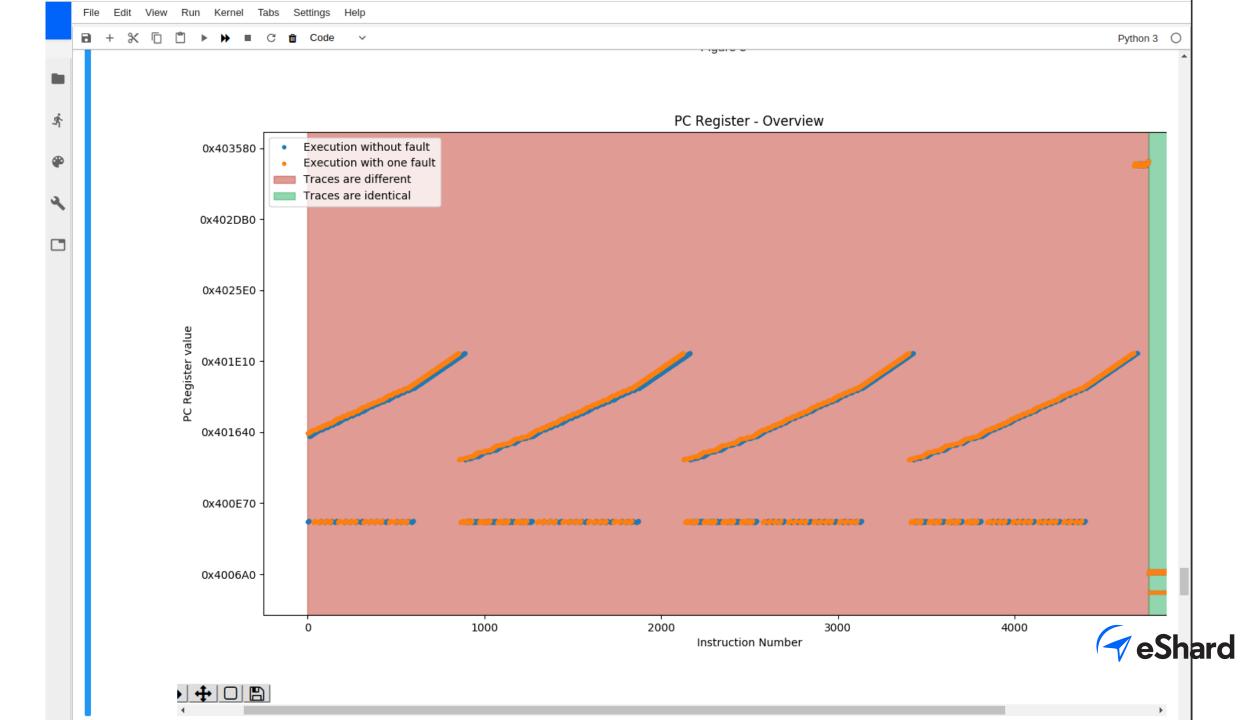
```
[35]: nb_inst_start_f1 = np.where(pc_np == 0x40138c)[0][0]
nb_inst_end_f1 = nb_inst_start_f1 + 1
pc_f1_start, pc_f1_end = int(pc_np[nb_inst_start_f1]), int(pc_np[nb_inst_end_f1])
pc_f1_start_count, pc_f1_end_count = len(np.where(pc_np[:nb_inst_start_f1+1] == pc_f1_start)[0]), len(np.where(pc_np[:nb_inst_end_f1])
```

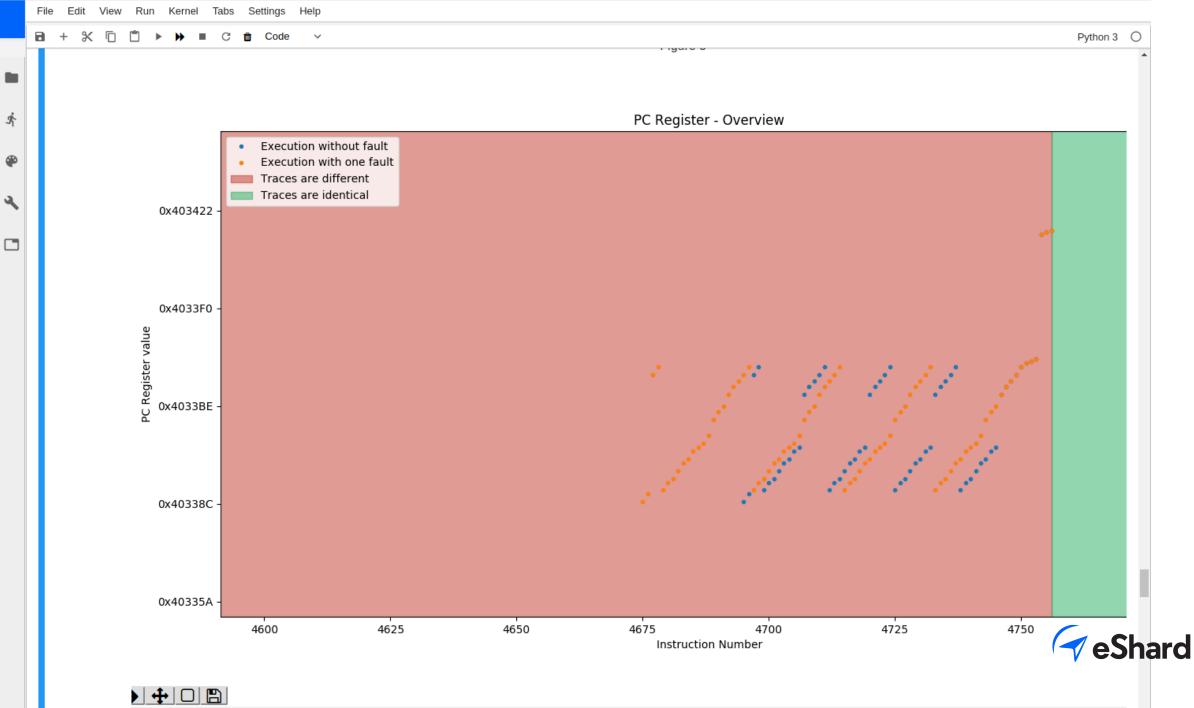
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<sup>[34]: &#</sup>x27;0x40138c'

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_				We modify the fault options:		*
				<ul> <li>we set the PC we want to fault</li> </ul>		
ŝ.				<ul> <li>we set the trig&amp;act</li> </ul>		
۲		[	36]:	<pre>fault_1.trig_and_acts = [[pc_start, pc_start_count, const.TAA_F_START_FAULT],</pre>		
٩				<pre>[pc_end, pc_end_count, const.TAA_F_STOP_FAULT]] fault_1.register = "rax"</pre>		
				We inject the fault.		
		[	37]:	<pre>faulter.directory_out = "fault_1_rax_with_trace" faulter.generate_faults(plain_input)</pre>		
				Injecting 1 faults (estimation). Please wait Processing 100.0 % Elapsed: 00:00:00 Remaining: 00:00:00		-



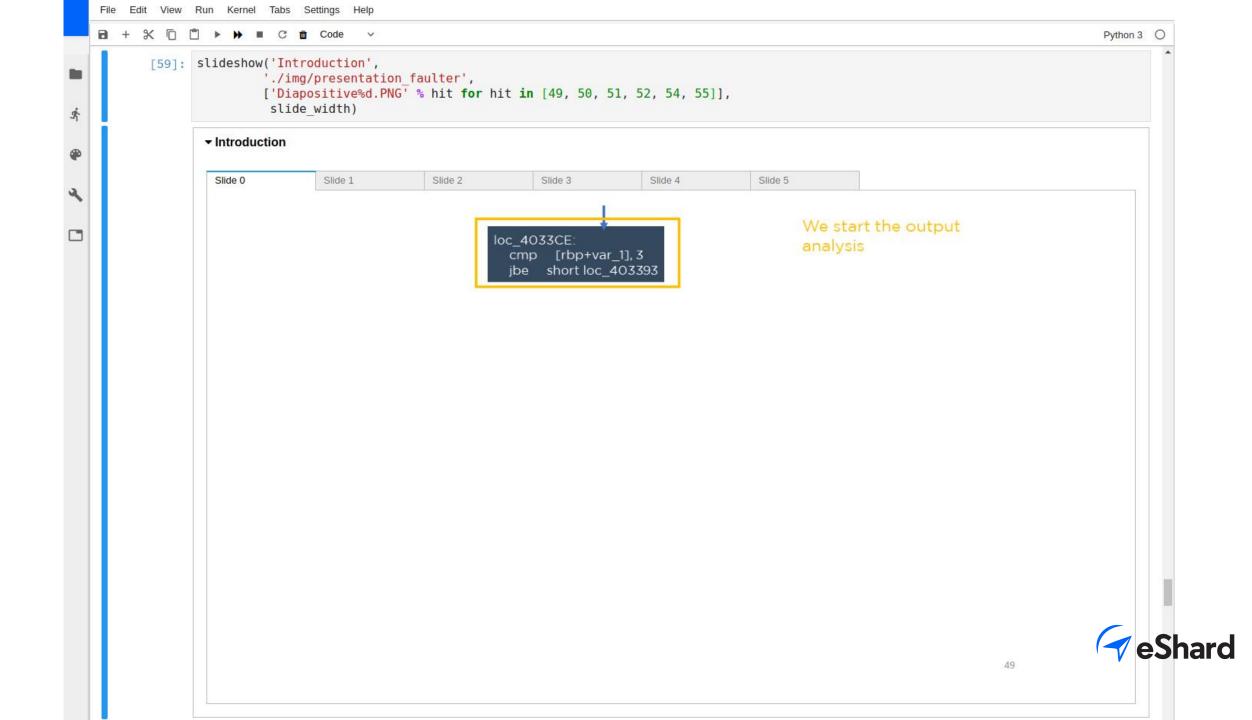


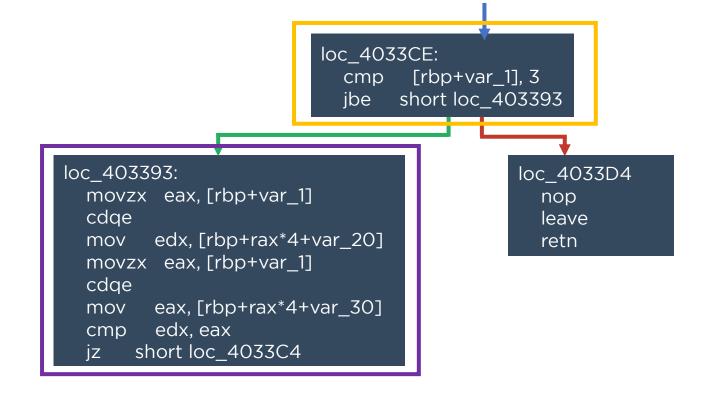


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	■ faulter_presentation.ipynb	
	$\blacksquare + \% \square \square \Rightarrow \Rightarrow \blacksquare \bigcirc  Code \lor$ Pytho	on 3 🔿
	What do we observe?	•
÷fr	<ul> <li>There are two distincts areas</li> <li>At the right, the points are superposed: both traces have the same execution</li> </ul>	
۲	<ul> <li>But at the left, there is a different behavior</li> <li>We can look the PC of the executed instructions located in range [4675, 4750], and see the corresponding instructions</li> <li>In that way, we might understand what happens.</li> </ul>	



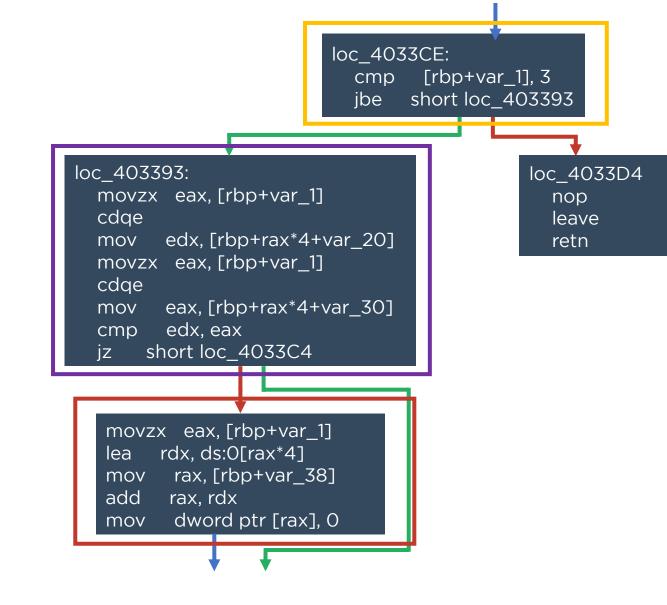




### We start the output analysis

The output was computed twice. Its consistency is checked by block of four bytes.



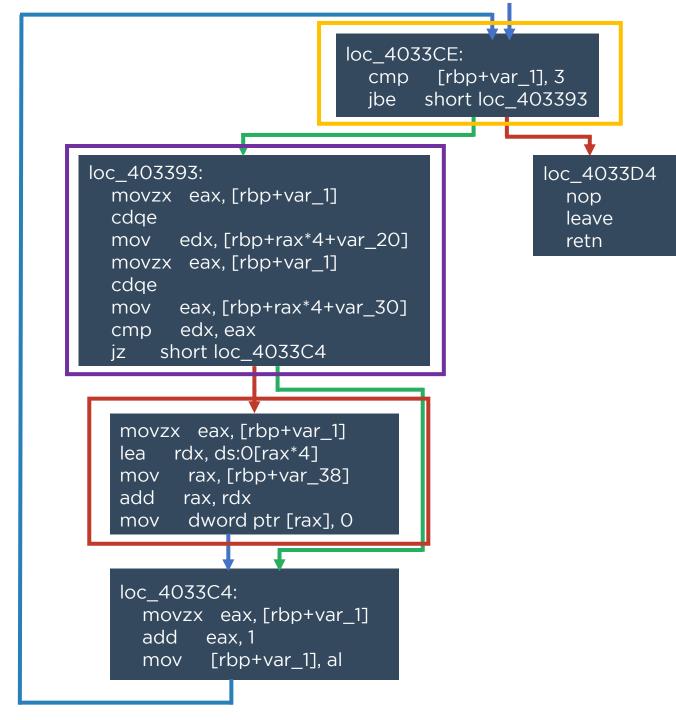


### We start the output analysis

The output was computed twice. Its consistency is checked by block of our bytes.

In case of a failure, the four-byte block is set to zero.





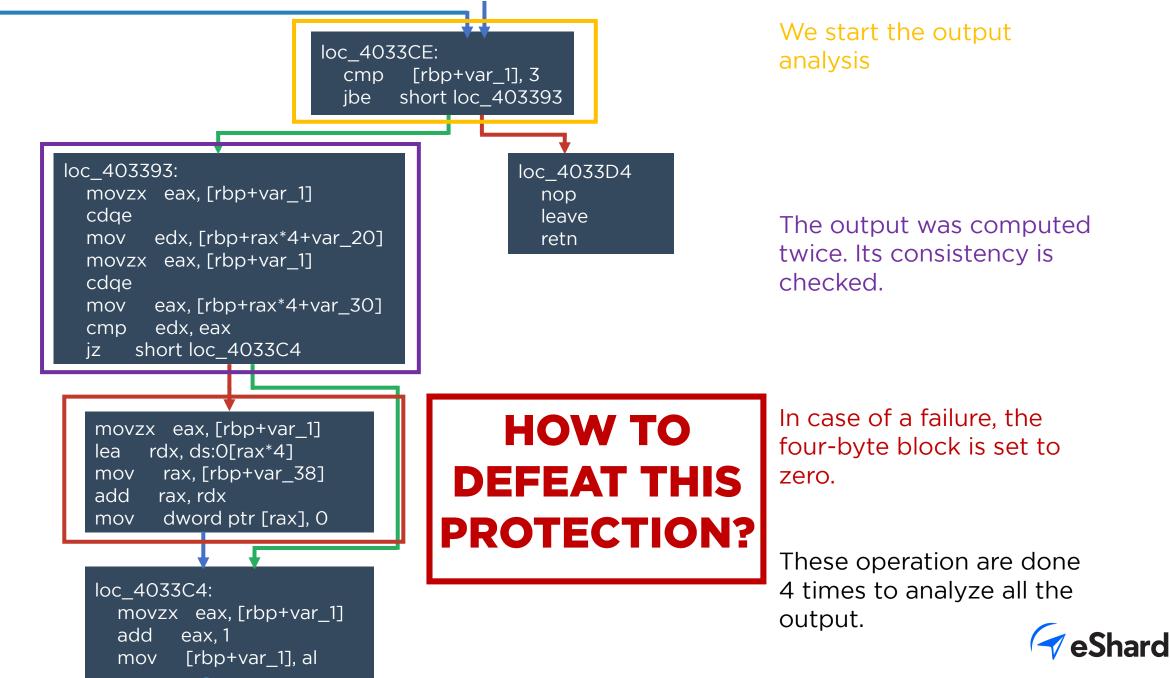
### We start the output analysis

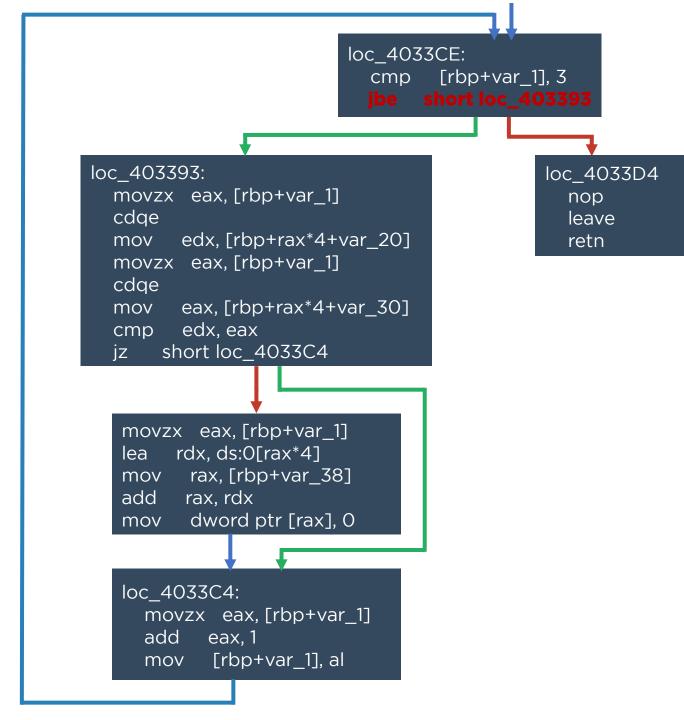
The output was computed twice. Its consistency is checked.

In case of a failure, the four-byte block is set to zero.

These operation are done 4 times to analyze all the output.



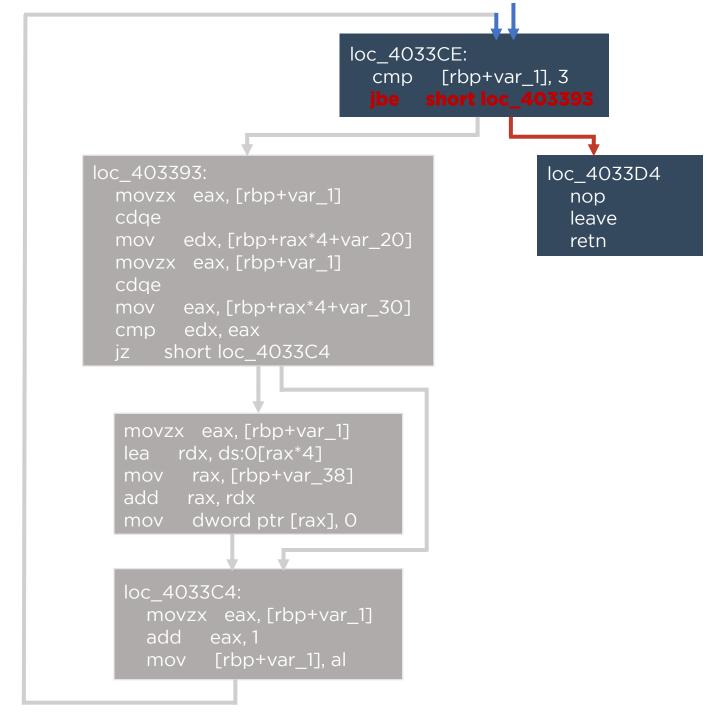




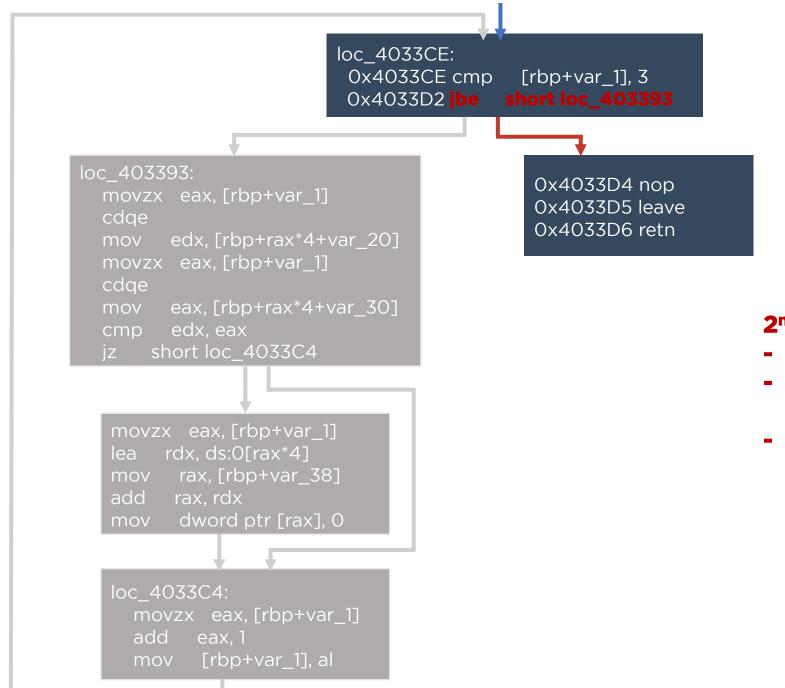
We can disturb the control flow:

- We jump from jbe instruction directly to 0x4033D4
- We exit the function
- No check is done
- No byte is set to zero









#### 2<sup>nd</sup> fault paramer:

- Register: Program Counter
- When: instruction located at 0x4033D2 is reached
- Fault model: Add user value + 2 to jump directly to 0x4033D4



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	A
2nd FAULT INJECTION	
We are going to inject two faults:	
the first fault aims to set the output to zero	
<ul> <li>the second fault targets the consistency verification.</li> </ul>	
We remove the fault&trace option.	
[ ]: faulter.tracer = None	
We will attack only the rax register, and target only the PC that enabled to have an output set to zero.	
<pre>[]: pc_to attack = []</pre>	
<pre>for hit in parser.get_fault_dico()["OUTPUT"]["00000000000000000000000000000000000</pre>	
We configure the first fault.	
<pre>[ ]: fault_1.register = "rax"</pre>	
<pre>fault_1.filt_pre.in_pc.pts = pc_to_attack[0:32] fault_1.trig_and_acts = [trig_and_act_start_fault_injection,</pre>	
trig_and_act_stop_fault_injection]	
We change the directory.	
<pre>[ ]: faulter.directory_out = "faulter_outputs_two_fault"</pre>	
We configure the second fault:	
we want to modify the PC register	
<ul> <li>when its value is 0x4033D2, we add 2 to it to skip an instruction and jump directly to 0x4033D4</li> </ul>	
<pre>[ ]: fault_2 = FaultCfgMono(bin_hand)</pre>	9
<pre>[ ]: fault_2.filt_pre.in_pc.pts = [0x4033D2]</pre>	
<pre>fault_2.register = "pc" fault_2.param = 2</pre>	
<pre>fault_2.param = 2 fault_2.model = const.FM_ADD_USER_VAL</pre>	

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		We set the two faults	*
÷fr	[50]:	<pre>faulter.faults_opt = [fault_1, fault_2]</pre>	
	[51]:	faulter.generate_faults("061FC9F588B2F9D2001986822C121179")	
۲		Injecting 6 faults (estimation). Please wait	
ð		Processing 100.0 % Elapsed: 00:00:00 Remaining: 00:00:00	



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		We analyse the new log.		*
°¢7	[77]:	<pre>logfile = glob.glob("./%s/*.bin" % faulter.directory_out)[0] parser = FaulterLogParser(logfile, output_parser = bin_hand.process_output)</pre>		
۲		<pre>parser.parse_file() # Print faulty outputs with 4 modified bytes parser.get faulty output([4])</pre>		
ď				
	[77]:	<pre>[['14ed0lea7ce2a551c9791ae85c7cecf4', '6AED01EA7CE2A570C979D3E85CD2ECF4'], ['14ed0lea7ce2a551c9791ae85c7cecf4', '4EED01EA7CE2A5AEC97950E85C40ECF4'], ['14ed0lea7ce2a551c9791ae85c7cecf4', '14201EA7CE2A5AEC97950E85C40ECF4'], ['14ed0lea7ce2a551c9791ae85c7cecf4', '142301EA11E2A551C9791AC25C7C67F4'], ['14ed0lea7ce2a551c9791ae85c7cecf4', '142B01EA7EE2A551C9791A575C7C09F4'], ['14ed0lea7ce2a551c9791ae85c7cecf4', '147101EAC2E2A551C9791AA5C7C90F4'], ['14ed0lea7ce2a551c9791ae85c7cecf4', '145E01EA6CE2A551C9791A555C7C53F4'], ['14ed0lea7ce2a551c9791ae85c7cecf4', '14ED09EA7C97A551CE791AE85C7CEC98'], ['14ed0lea7ce2a551c9791ae85c7cecf4', '14ED09EA7C97A551CE791AE85C7CEC98'], ['14ed0lea7ce2a551c9791ae85c7cecf4', '14ED02EA7C2A55163791AE85C7CEC30'], ['14ed0lea7ce2a551c9791ae85c7cecf4', '14ED02EA7C35A55146791AE85C7CEC30'], ['14ed01ea7ce2a551c9791ae85c7cecf4', '14ED01e57CE2D551C9F61AE8F77CECF4'], ['14ed01ea7ce2a551c9791ae85c7cecf4', '14ED01E17CE2EF51C91B1AE88C7CEC59'], ['14ed01ea7ce2a551c9791ae85c7cecf4', '14ED01E17CE2EF51C91B1AE88C7CECF4'], ['14ed01ea7ce2a551c9791ae85c7cecf4', '14ED01E17CE2EF51C91B1AE88C7CECF4'], ['14ed01ea7ce2a551c9791ae85c7cecf4', '14ED01E47CE2451C9951AE857CECF4'], ['14ed01ea7ce2a551c9791ae85c7cecf4', '14ED01E47CE2451C9951AE857CECF4'], ['14ed01ea7ce2a551c9791ae85c7cecf4', '14ED01E47CE24551C9951AE857CECF4'], ['14ed01ea7ce2a551c9791ae85c7cecf4', '14ED01E47CE22551C9951AE857CECF4'], ['14ed01ea7ce2a551c9791ae85c7cecf4', '14ED01E47CE22551C9951AE857CECF4'], ['14ed01ea7ce2a551c9791ae85c7cecf4', '14ED01847CE22551C9951AE857CECF4'],</pre>		

This time, we have faulty outputs with 4 modified bytes.



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#### Key recovery

[53]: from estoolkit2.fault\_attack.aes import DfaAesPiret

# We perform the DFA of Piret
# We need output with 4 faulty bytes
lst = parser.get\_faulty\_output([4])
dfa = DfaAesPiret(lst)
dfa.run()

found\_key = dfa.get\_found\_key()
dfa.summary()

**DFA** summary:

Words 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

Found bytes 0x97 0x78 0x13 0xD5 0x5A 0x7B 0x8A 0xE1 0x55 0x8C 0x31 0x3C 0xA0 0xBA 0x7E 0x6D

We found the round key 0x97 0x78 0x13 0xD5 0x5A 0x7B 0x8A 0xE1 0x55 0x8C 0x31 0x3C 0xA0 0xBA 0x7E 0x6D. However, we still need to recover the master key, and then check it.

To recover the master key, we need:

- · the extracted round key,
- its round number,

[54]: from essva.tools.tools import inv\_key\_schedule\_aes master\_key = inv\_key\_schedule\_aes(round\_key=found\_key, round\_nb=10, verbose = True)

Computed AES Master Key:

Words	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Round key 10	0x97	0x78	0x13	0xD5	0x5A	0x7B	0x8A	0xE1	0x55	0x8C	0x31	0x3C	0xA0	0xBA	0x7E	0x6D
Computed Master Key	0x 1	0x12	0x23	0x34	0x45	0x56	0x67	0x78	0x89	0x9A	0xAB	0xBC	0xCD	0xDE	0xEF	0xFF



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			We found a l	key! Le	et's tes	st it.													
	[5	5]:	<pre>message = res = bin_</pre>									sage	=mess	age)					
			Key matchin	g: SU(	CCES	S													
			Words 0	1	2 3	3 4	5	6	7	8	9	10	11	1	2 1	31	4 1	.5	
			Key bytes 0x01	0x12	0x23 0	)x34 0	x45 0	x56 0x	67 Ox	78 Ox	(89 Ox	9A 0x	AB 0x	BC 0	CD 0	xDE 0	xEF 0	xFF	
			Words	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
			Plaintext	0x09	0x6F	0x90	0x87	0x07	0x68	0x82	0x1A	0x84	0xBD	0x66	0x5C	0x2F	0x7D	0xDD	0x4A
			Binary ciphertext	0x39	0x5A	0xF9	0x24	0xAC	0xB5	0x2E	0xB5	0xA2	0xDC	0x8F	0xAF	0xFC	0x41	0xDC	0x58
										0.05				0.05	0.00	0	0	0xDC	0



[56]: sli	['Diap	/presentation_	faulter', % hit <b>for</b> hit <b>i</b> n	[61,62,63 ]],		
- C	Conclusion					
s	lide 0	Slide 1	Slide 2			
				AGENDA		
	•	Intr	oduct	ion		
	•	How	ı to fa	ult a White	e-Box	
	•	Req	uirem	ents to fau	lt a White	-
		Box				
	٠	Dou	ble Fa	ult injectio	on on an	
		AES	Whit	e-Box		
	•	Con	clusio	n		G

## CONCLUSION

Faulting a White-Box must be focused on the binary.

**4** 

Dynamic fault injection is a prerequisite



Accurate multiple faults can be injected



Security mechanisms can be defeated

But

Combinatorial complexity

nb\_ins x nb\_fault\_model x nb\_target x
nb\_input x nb\_area

### CONCLUSION

Strategies to defeat these issues:



Pattern detector to fault only interesting area



Focus faulting on specific Register / Program Counter or instructions



Fault & trace to understand the effect of a fault or downgrade security

In that way, it is possible to execute successful multi-fault attacks, in reasonable amount of time.

